EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	control and circuit and coupled and main and memory and cache and memory and comparing and size and cache and memory and product and data and length and N and desired and interleaving and de-interleaving and depth and D and produce and control and signal and writing and bytes and each and first and word and into and either and main and memory and cache and memory and according and control and signal and reading and bytes and out and cache and memory and main and memory and forming and each and second and word and according and control and signal and each and second and word comprises and bytes and more and than and one and first and words.clm. and (714/701 or 711/127 or 714/786).ccls.	US-PGPUB	OR	ON	2009/06/30 17:44
S1	1	control and circuit and coupled and main and memory and cache and memory and comparing and size and cache and memory and product and data and length and N and desired and interleaving and de-interleaving and depth and D and produce and control and signal and writing and bytes and each and first and word and into and either and main and memory and cache and memory and according and control and signal and reading and bytes and out and cache and memory and main and memory and forming and each and second and word and according and control and signal and each and second and word comprises and bytes and more and than and one and first and words.clm.	US-PGPUB	OR	ON	2009/06/04 18:06